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Application Number:	10715267
International Application Number:	
Confirmation Number:	4590
Title of Invention:	Methods for polishing semiconductor device structures by differentially applying pressure to substrates that carry the semiconductor device structures
First Named Inventor/Applicant Name:	Nathan R. Brown
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Reply Brief Filed	ReplyBrief.pdf	271812 21affd3322ffd4fe3c22414fd8a328d60f6 48585	no	9

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